

ABSTRACT OF THE DISCLOSURE

Performance control of a processor core 52 is achieved by modulating between a
5 processing mode power supply configuration which the processor core 52 is clocked and
a holding mode power supply configuration which the processor core 52 is not clocked.
By modulating between these two power supply configuration modes, a target
performance level may be achieved and energy consumption whilst in the holding mode
can be reduced.

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[Figure 8]